Willy Sansen 教授高级模拟集成电路设计课程

--Advanced Analog Circuit Design 2014

(4th-6th December 2014)

Why Participate:

The course will discuss the most important practical problems in micro-power and low-voltage circuits facing analog IC designers and their solutions on an advanced level. Among others, the course will cover low-voltage, basic circuit techniques, special low-power, Stability of operational amplifiers, bandgap references, A/D converters and communications circuits.

Design of analog circuit blocks is a moving target because of the continuing scaling of power supply voltage, and because of the changing behavior of devices with scaling of technology. Willy Sansen, prof. em. at KU Leuven and former president of IEEE Solid-State Circuits Society, has updated his Analog Design Essentials lectures. Take the opportunity and learn how to make your analog circuits perform!

Who Should Attend:

This course has been developed for several categories of designers:

- Managers of design teams of analog IP blocks and circuits, and their designers.
- Novice designers to learn about design plans, such that they can master the art
 of optimum design for low-power and low-noise.
- Digital designers to be able to design and to include low-power analog blocks on digital system chips.
- Designers with analog experience, to update their design knowledge and to tune their experience to the present-day design procedures.





Why Lynne Consulting:

Lynne Consulting is offering advanced engineering courses in the field of analog, RF and mixed-signal IC design targeting the audience of electrical engineers, company managers and marketing engineers working in the semiconductor industry. The lecturers are leading practitioners and top experts in the area from high-technology companies and universities, who teach the most up-to-date information available at the time of the course.

Comments from Past Delegates:

- 'A very useful course full of interesting ideas. Prof. Sansen's knowledge of the subject and mental ability is second to none.'
- 'It is very good to be lectured by one of the world-class experts in this area. Profesor Sansen has presented a difficult design topic in an easy, interesting and stimulating manner.'
- 'Well presented, well organized, up-to-date with excellent example of practical design, probably one of the best course on design you can get.'
- 'I would recommend this course to anyone seeking to gain knowledge on the analog design process especially MOST.'
- 'I would recommend it to anyone wishing to gain knowledge in analogue VLSI.'

Companies & Universities That Have Sent Delegates:

- 上海华虹集成电路有限责任公司
- 中颖电子股份有限公司
- ADT
- 上海贝岭股份有限公司
- 上海集成电路研发中心有限公司
- 中国科学院上海高等研究院
- CETC 第十四研究所
- 南京微盟电子有限公司
- 中国科学院上海微系统与信息技术研究所
- 西安太乙电子有限公司
- 西安邮电大学
- 南通大学
- 中芯国际

- 上海华力微电子有限公司
- 复旦大学
- 北京时代民芯科技有限公司
- 上海华虹宏力半导体制造有限公司
- 厦门市芯阳科技有限公司
- 艾迪悌科技(上海)有限公司
- · CETC 第五十八研究所
- 昆明物理研究所
- 记忆科技(深圳)有限公司
- 国民技术股份有限公司
- 北京微电子技术研究所
- 中国科学院上海技术物理研究所
- 中国科学技术大学

Course Details:

- Duration: 3 days (4th-6th December 2014)
 - Location: Building 21, No 1388, Zhangdong Road, Pudong New District, Shanghai, China
- Fees: ¥3800/person
 - A discount applies for groups before 23 November 2014 (2 persons (Total: ¥7380); 3 persons (Total: ¥10800); 4 persons 人 (Total: ¥13980); 5 persons or more (negotiation))
- ¥2800/person for students
- The above discount can not apply simultaneously
- Online Registration(<u>www.lynneconsulting.com</u>)
- Contact us(Steven.Yu,021-58978665,Email:steven.yu@lynneconsulting.com)

The Course Schedule:

THEME 1: NOISE & OFFSET

Day 1
4th December 2014

- Noise performance of elementary transistor stages
- Stability of operational amplifiers
- Systematic design of operational amplifiers
- Feedback : Voltage and transconductance amplifiers

THEME 2: AMPLIFIERS

Day 2

5th December 2014

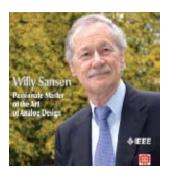
- Feedback : Transimpedance and current amplifiers
- Offset and CMRR: random and systematic
- Bandgap and current reference circuits
- Principles of ADCs and DACs

THEME 3: ADC'S & RF CIRCUITS:

Day 3
6th December 2014

- Low-power Sigma-Delta AD converters
- High-Speed Continuous-time Sigma-Delta converters
- Design of crystal oscillators and VCO's
- High speed synthesizers and PLLs

Lecturer's Biography:



Willy Sansen 教授于 1972 年从加州大学伯克利分校获得博士学位,从 1980 年起在比利时天主教鲁汶大学担任全职教授。从 1984 年到 2008 年,Willy Sansen 教授担任 ESAT-MICAS 实验室的模拟设计带头人。他指导过 63 名博士,发表过 635 篇文章,出版了 6 本专著。他是 IEEE 固态电路协会的前主席,并担任过 2002 年国际固态电路会议的程序委员会主席。

Willy Sansen 教授分别于 1978 年在斯坦福大学,1981 年在洛桑联邦理工学院,1985 年在美国费城宾夕法尼亚大学,1994 年在 T.H. Ulm,2004 年在菲拉赫英飞凌任客座博士。

2011 年,Willy Sansen 教授由于在固态电路领域做的突出贡献被 IEEE 固态电路协会授予 Donald O. Pederson 奖。Donald O. Pederson 奖是 IEEE 固态电路的一个技术领域奖,该奖每 年由 IEEE 固态电路委员会颁发给那些"对固态电路领域有突出贡献"的人。同时他还是 IEEE 的终身 Fellow。



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• Shanghai • China

• Phone: +86 21 5109 6090

• Fax: +86 21 3327 5892

www.lynneconsulting.comAddress: C-819, Tomson Center,